

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:  
WARREN M. FARNWORTH  
SALMAN AKRAM

Serial No.:

ART UNIT: 2858

Filing Date: April 27, 2001

EXAMINER: KORBERT, R.

Division of Serial No. 09/266,237 filed on March 10, 1999

For: TEST INTERCONNECT FOR BUMPED SEMICONDUCTOR  
COMPONENTS AND METHOD OF FABRICATION

Attorney Docket No. 97-1433.1

**INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR §1.56**

**April 27, 2001**

Assistant Commissioner of Patents  
BOX PATENT APPLICATION  
Washington, DC 20231

Sir:

The citations listed on the accompanying Form PTO/SB/08, copies attached, may be material to the examination of the subject application and are therefore submitted in compliance with the duty of disclosure defined in 37 CFR §§1.56, 1.97 and 1.98.

This Information Disclosure Statement under 37 CFR §§1.56, 1.97 and 1.98 is not to be construed as a representation that a search has been made, that additional material to the examination of this application does not exist, or that any one or more of these citations constitutes prior art under 35 USC §102.

It is requested that the above citations be made of record, if appropriate, in the prosecution of this application.

Respectfully submitted:



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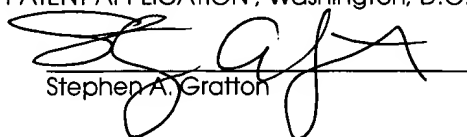
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I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Address" Service under 37 CFR 1.10 on the date indicated above and is addressed to the Assistant Commissioner of Patents, BOX PATENT APPLICATION, Washington, D.C. 20231.

April 27, 2001  
Date of Signature



Stephen A. Gratton